







- 4) APPLY SOLDERMASK OVER BARE COPPER, BOTH SIDES.
FINISH ALL EXPOSED COPPER SURFACES WITH 60/40% TIN/LEAD SOLDER, 100u" TO 1000u".
- 5) THE LOCATION OF THE MARKING SHOULD BE ON THE SURFACE OF THE PCB, NOT THE EDGE.
- 6) APPLY SOLDERMASK TO COMPONENT SIDE AND SOLDER SIDE, USING
☐ PC401 OR EQUIVALENT.
☒ LIQUID PHOTO IMAGEABLE.
☐ DRY FILM.
COLOR : GREEN.
USE SEPARATE FILM FOR COMPONENT SIDE AND SOLDER SIDE SOLDERMASKS.
FABRICATOR SHALL MAKE NECESSARY MODIFICATIONS TO SOLDERMASK PHOTO PLOT FILES FOR OPTIMAL SOLDERMASK COVERAGE BETWEEN FINE PITCH COMPONENT LEADS.
- 7) APPLY LEGEND TO COMPONENT SIDE USING NON-CONDUCTIVE EPOXY INK.
COLOR : WHITE.
FABRICATOR SHALL MAKE NECESSARY MODIFICATIONS TO LEGEND PHOTO PLOT FILES TO ENSURE NO LEGEND INK COVERS ANY COMPONENT PAD OR VIA PAD.
- 8) PCB SHALL BE CLEAN AND FREE FROM DIRT, OIL, FINGERPRINTS, CORROSION, AND ANY OTHER FOREIGN MATERIAL.
- 9) MODIFIED PHOTO PLOT FILES ARE TO BE RETURNED BEFORE ORDER DELIVERED.
- 10) FABRICATOR HAS APPROVAL TO REMOVE NON-FUNCTIONAL PADS FROM INNER LAYERS.
- 11) BOW AND TWIST SHALL BE LESS THAN 0.005" PER INCH.
- 12) ALL PRINTED CIRCUIT BOARD NETS SHALL BE 100% ELECTRICALLY TESTED FOR OPENS AND SHORTS. MARK TEST VERIFICATION STAMP APPROXIMATELY WHERE SHOWN, ALONG EDGE OF PCB, USING WHITE INK.
- 13) FABRICATION OF P.W.B. TO COMPLY WITH IPC-A-600, CLASS II , CURRENT REVISION.
- 14) CUT J23 PIN 6.

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| | APPROVALS | | DATE | | ZF Embedded, Inc. | | | 1052 Elwell Court Palo Alto, CA 94303 (650) 965-3800 | |
| | DRAWN | Silvi Keskula | 05/05/2000 | | | | | | |
| | CHECKED | Kaido Kevvai | 05/05/2000 | | MACHZ EVALUATION 1 Assembly Drawing Bottom | | | | |
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| | | CLIENT ZF Embedded, Inc. | | | | SIZE E | DRAWING NUMBER 9400-0038-03 | SCALE 1 : 1 | SHEET 2 OF 2 |